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PRIOR ART

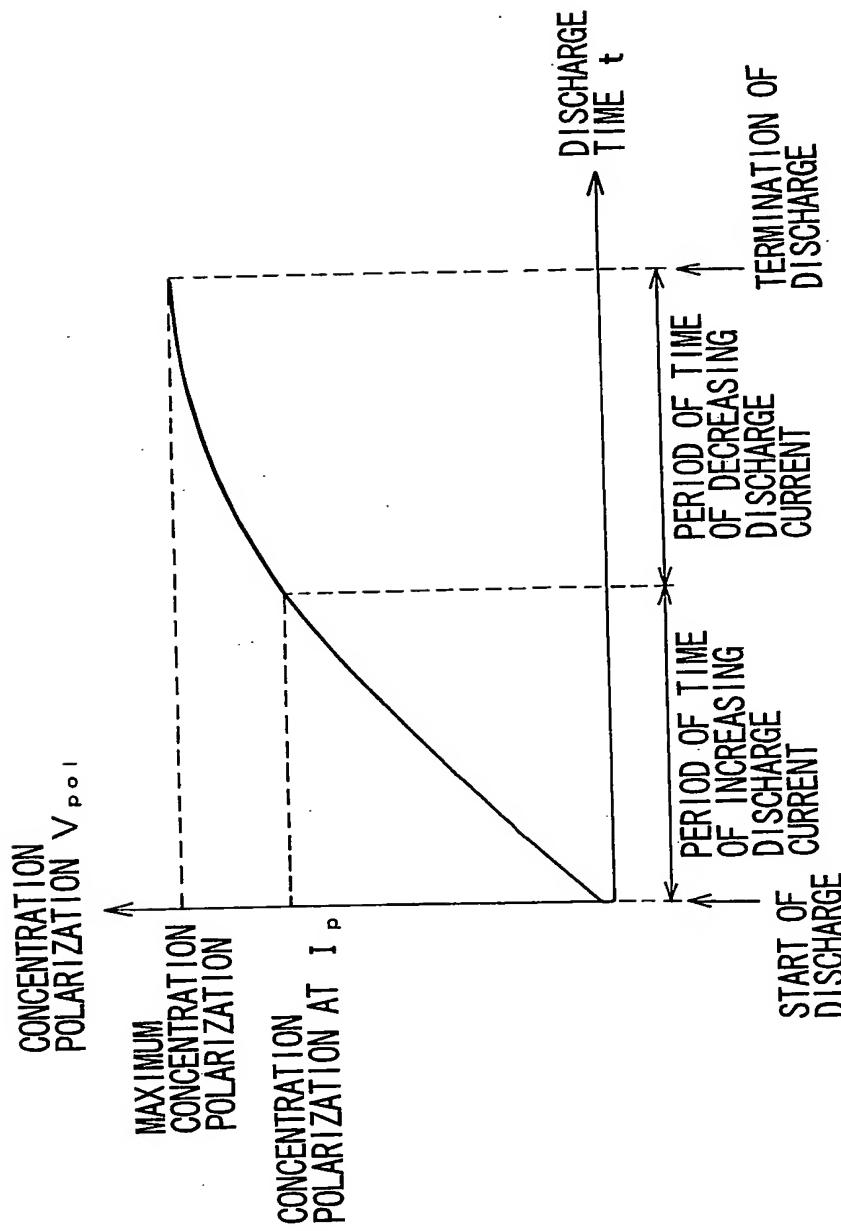


FIG. 6 A

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PRIOR ART

TERMINAL VOLTAGE V

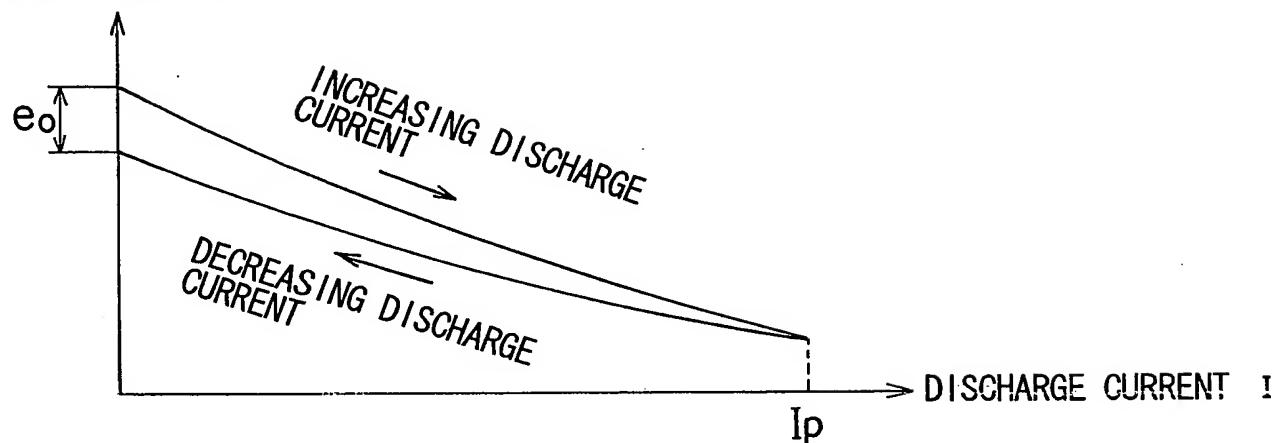


FIG. 7